

## ▶ CPU AHS

## CHARACTERISTICS



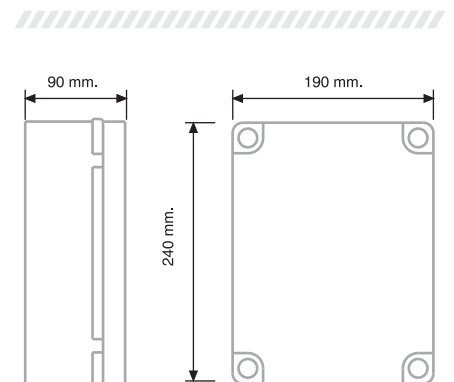
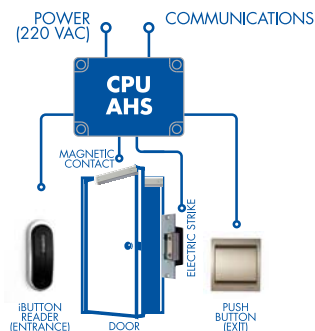
**DORLET's CPU AHS is a high range system for access and Time & Attendance control. It allows management of one door in a single direction, with control over all the required signals (door magnetic, exit push button and lock). Direct connection to Ethernet (10 Mbps) for on-line mode or DOC (DORLET ON CARD) management for off-line. The most common applications of the CPU**

- Complete control of 1 access and/or Time & Attendance reader.
- High-capacity per reader: 500 employee cards, 95 visitor cards, 800 messages...
- 1 output (configurable with/without voltage) for electric lock.
- 1 input for magnetic contact, 1 pushbutton input for opening the door, 1 tamper input.
- Centralised LAN operation or off-line DOC (DORLET ON CARD)..

AHS are as follows:

- Access control.
- Time & Attendance (employee clock-in).
- Visits control.

In on-line mode, the entire configuration of the CPU (parameters, cards, permits, schedules, events, etc.) is stored in the CPU's memory, achieving a highly flexible operating system. Likewise, the management possibilities in DOC mode (DORLET ON CARD) allow it to adapt to any kind of installation and/or architecture.



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## ▶ TECHNICAL SPECIFICATIONS

### ▶ PHYSICAL

Box dimensions:	240 x 190 x 90 mm.
CPU's weight:	1 Kg. (without battery)
Protection:	IP56

- The CPU box has a space for housing the 12 VDC and 2 H battery.

### ▶ POWER SUPPLY

Input:	220 VAC 10%, 50/60 Hz with ground cnt. and interference suppressor filter
Consumption:	5 VA
Output:	5 VDC/1 A; 12 VDC/1A (readers and locks) and 13.8 VDC for battery.
Detection:	Failure or low battery tension, low board output, failure in the Power Supply.

- The CPU provides power to the reader, lock & associated sensors (see consumptions of each element).

### ▶ COMMUNICATIONS

RS-232:	Conf. up to 115,200 bauds (connection via GSM modem, RTC, GPRS, etc.)
TCP-IP:	TCP-IP: 10 Mbps, RJ-45 connector, Telnet server for communications setup

### ▶ ELECTRONICS

Micro-controller:	Microchip PIC 24FJ256GA106
Specifications:	32 Mhz, core 16 bits
Memory:	RAM 32 Kbytes
Program memory:	Flash 256 Kbytes
Data retention:	Battery for preserving RAM (2 months)
Remote update:	Yes
Clock:	In real time

- Software in high level language over multitasking operating system.
- RAM memory with battery for power failures and data retention.

### ▶ INPUTS AND OUTPUTS

Reader inputs:	1 (door with reader for entrance)
Digital inputs:	3 (1 pushbutton, 1 magnetic contact, 1 tamper)
Lock outputs:	1 (configurable NO/NC and with/without voltage)
Digital outputs:	1 (forced or left open door detection)
Supply outputs:	5 VDC and 12 VDC

- The inputs and outputs may vary its use according to CPU's setup.

### ▶ CAPACITIES (in LAN mode)

- **Employee cards: 500**
  - **Visitor cards: 95**
  - **Access messages: 800**
  - **Schedules with 3 working days.**
  - **Automatic opening schedules.**
  - **PINs elimination timetables.**
- (Standard capacities, consult additional configurations)

- Intimidation (through software)
- Forced door detection
- Door left open detection
- Bistable cards
- Special cards
- ...

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